JUN 1 0 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In application of:

Timothy E. Fiscus et al.

"Serial No.:

10/629,667

Art Unit:

2818

Filed:

July 29, 2003

Examiner:

Mai, S.

For:

METHOD AND ARCHITECTURE FOR REDUCING THE POWER

CONSUMPTION FOR MEMORY DEVICES IN REFRESH

OPERATION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SUBMISSION OF DRAWING AMENDMENT (37 CFR 1.121)

Attached please find

(check applicable items)

[X] replacement drawing sheet(s) in permanent ink

[] annotated marked-up copy of the amended drawing(s) with annotations showing the change(s).

CHRISTOPHER P. MAIORANA, P.C.

Robert M. Miller

Registration No. 42,892

24840 Harper Avenue, Suite 100 St. Clair Shores, Michigan 48080

(586) 498-0670

Dated: June 4, 2004

CERTIFICATE OF MAILING (37 CFR 1.8(a))

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail, in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Jan M. Dunbar

Date: June 4, 2004